

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert M. Wolrich et al. Art Unit : Unknown
Serial No. : Not Yet Assigned Examiner : Unknown
Filed : March 16, 2001
Title : **LOCAL REGISTER INSTRUCTION FOR MICRO ENGINE USED IN
MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE**

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

The Title:

Please amend the title throughout to read:

“REGISTER INSTRUCTIONS FOR A MULTITHREADED PROCESSOR”

In the specification:

On page 11, between lines 6-17, please add the following paragraph to the ‘Detailed Description’:

-- The EBOX also includes two multiplexors, “mux A” and “mux B”, which provide a selection capability of the operands to be input to ALU 76a. The 32-bit wide output buses from several microengine data sources are connected to mux A and mux B, including an IMMEDIATE_DATA bus from the control logic 72, a write-back data bypass bus from the

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Year	1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100
1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	

REMARKS

Applicant has amended the title of the continuing application to better reflect the invention as claimed. This amendment to the title does not add new matter.

Applicant has also amended the detailed description. This amendment does not add new matter, it merely describes the elements included in the EBOX as shown in FIG. 3. The additional paragraph is supported by the drawings filed with the original application (this application is a continuation application of and claims priority to PCT Application Serial No. PCT/US00/24054 filed on September 1, 2000). Additional support for the amendment is found at page 12, lines 17-24, of the specification, which describes the controlling and shifting of 8-bit bytes within a 32-bit operand.

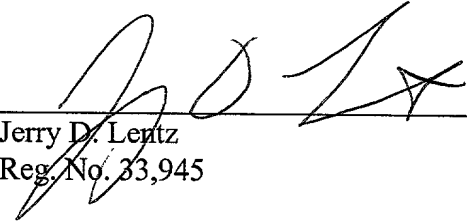
Applicant submits that the specification and claims are now in condition for examination, which action is requested. Filed herewith is a check in payment required by the above amendment. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: _____

3/19/2001

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